

09/980027

JC13...d PCT/PTO 26 NOV 2001

Our Ref.: 15675.P382  
Express Mail No. EL651893309US

UTILITY APPLICATION FOR UNITED STATES PATENT  
FOR

**A BANDPASS FILTER WITH CARRIER FREQUENCY REDUCTION**

Inventors: Dominique Morche

2020/2020-20008660

9/P&amp;V 1

A BANDPASS FILTER WITH CARRIER FREQUENCY REDUCTION

The invention relates to analog bandpass filters, and more particularly to those which present high selectivity at high frequencies, typically several hundreds of megahertz (MHz).

The invention thus relates in particular to front end architectures for receivers or transmitters of radio frequency (RF) signals.

One of the applications of the invention lies in integrating an analog portion of a GSM type mobile terminal receiver, where the purpose of the analog portion is to amplify the signal received by the antennas at a very high frequency, so as to select a frequency band of interest to the user of the terminal, and so as to reduce this frequency band to a low frequency.

In conventional manner, a frequency band is selected by filtering. The usual integrated filtering does not enable very high quality (Q) factors to be obtained. In order to select a narrow band corresponding to the frequency band of a user, it is therefore generally necessary either to use an external filter known as a surface acoustic wave (SAW) filter or to decrease the carrier frequency of the signal.

Such an operation of decreasing the carrier frequency, known as frequency transposition, is generally performed by means of analog multipliers. One of the major problems with such frequency transposition lies in the formation of an image frequency in addition to the desired signal. That is why known devices do not make it simple to decrease frequency without using an external filter (in particular a SAW filter) which serves to eliminate the image frequency.

To make a narrow filter for a channel situated at high frequency, French patent application FR 95/05847 proposes a circuit such as that shown in Figure 1 in which the same input signal for filtering is subjected to a frequency transposition on each of two parallel

branches, followed by lowpass filtering and followed by further frequency transposition to the original frequency of the input signal. In order to prevent the image frequency from being folded into the working channel, the  
5 four transpositions that are performed make use of only two signals which are in phase quadrature, and each of these two signals is applied to each of the two branches in a cross-over configuration enabling any phase shift differences to be compensated.

10 That document also proposes using an RC-CR phase shifter of the kind shown in Figure 2, which guarantees that the two signals are in quadrature. Nevertheless, when the frequency of an oscillator placed at the input of the phase shifter is slightly different from the  
15 cutoff frequency of the phase shifter, then the two output signals are not equal in amplitude.

That phenomenon is compensated in the prior art device by crossing over the quadrature signals within the circuit.

20 Such crossing over of quadrature signals within a bandpass filter structure is also proposed in "A high-Q 200 MHz low-power fully-integrated bandpass IF filter", CICC'98.

25 Although those circuits enable filtering to be performed at a very high frequency, they do not enable the carrier frequency of the signal to be decreased (for a receiver) or increased (for a transmitter), even though four multipliers are used.

The object of the invention is to propose a method  
30 and apparatus for narrow filtering of a high frequency signal, also enabling the carrier frequency of the signal to be reduced while making use of a small number of multipliers only.

This result is obtained with a bandpass filter  
35 method in which two frequency transpositions of an input signal for filtering are performed in parallel, making use respectively of a first mixing signal and of a second

mixing signal which mixing signals are substantially in phase quadrature so as to obtain respective first and second transposed signals. The two transposed signals are filtered by two respective lowpass filters (with the 5 transposition signals being at a frequency and with the lowpass filters having a passband that are associated respectively with the frequency of the input signal and with the passband desired for the bandpass filter). Thereafter, respective frequency transpositions are 10 performed on the first and second filtered transposed signals using two respective output mixing signals, and the sum or the difference of the two signals obtained in this way is taken. These transpositions are characterized in that the output mixing signals are 15 selected to be different in frequency from the first and second transposition signals so that the output signal lies in a desired frequency range.

To achieve this object, the invention also provides a bandpass filtering method in which two frequency 20 transpositions are performed in parallel on an input signal for filtering using respective first and second upstream mixing signals that are substantially in phase quadrature so as to obtain respective first and second transposed signals, and the two transposed signals are 25 filtered respectively by two lowpass filters, the frequency of the transposition signals and the passband of the low-pass filters being related to the frequency of the input signal and to the passband desired for the bandpass filter, then respective frequency transpositions 30 are performed on the first and second filtered transposed signals using two respective downstream mixing signals, and the sum or the difference of the two signals obtained in this way is taken, the frequency of the output mixing signals is selected to be different from the frequency of 35 the first and second mixing signals so that the output signal is transposed into a desired frequency range, the method being characterized in that a common oscillator is

202520042000000000

used which is coupled with a first phase shifter to produce the upstream mixing signals and which is coupled with a second phase shifter to produce the downstream mixing signals, and in that the phase shifters are used in opposite manner on the first and second signals so that each of said first and second signals receives the phase-advanced output signal from one of the two phase shifters and the phase-delayed output signal from the other of the two phase shifters.

10 Other characteristics, objects, and advantages of the invention will appear on reading the following detailed description given with reference to the accompanying drawings, in which:

- 15        - Figure 1 shows the above-mentioned state-of-the-art circuit;

          - Figure 2 shows the above-mentioned state-of-the-art RC-CR circuit;

          - Figure 3 is a schematic of a circuit of the invention;

20        - Figure 4 is a schematic of an upstream phase shifter for the Figure 3 circuit; and

          - Figure 5 is a schematic of a downstream phase shifter for the Figure 3 circuit.

As shown in Figure 1, the prior art bandpass filter of bandwidth equal to B comprises an input terminal BE for receiving an input signal SE of angular frequency  $\omega_e$ , and two parallel processing paths VT1 and VT2. Each processing path has an upstream mixer MA1 (MA2) followed by a lowpass filter F1 (F2) connected to a downstream mixer MV1 (MV2). The respective outputs from the downstream mixers are connected to two inputs of a subtracter STR whose output is connected to the output terminal BS of the filter.

Transposition means MT are also provided, delivering two mixing signals SM1 and SM2 of angular frequency  $\omega_0$ , and substantially in phase quadrature. The first mixing signal SM1 is delivered to the upstream mixer MA1 and the

resulting transposed signal ST1, after being filtered by the filter F1, gives a filtered transposed signal STF1 which, after being transposed in the downstream mixer MV1 using the second mixing signal SM2 delivers a  
5 retransposed signal STFT1 to one of the inputs of the subtracter STR.

Similarly, the second mixing signal SM2 is delivered to the upstream mixer MA2 so as to enable the transposed signal ST2 to be obtained and, after filtering, the  
10 filtered transposed signal STF2. This signal STF2, after being transposed in the downstream mixer MV2 using the first signal SM1, provides the retransposed signal STFT2 which is delivered to the other input of the subtracter STR.

15 After the difference has been taken between the two signals STFT1 and STFT2, the output signal SSF is stripped of the undesired sideband centered on angular frequency  $2\omega_0 - \omega_e$ , which is equivalent to eliminating the influence of the image signal from the input signal.

20 As shown in Figure 3, the bandpass filter of the invention reproduces the same general structure with two parallel processing paths VT1 and VT2, each having an upstream mixer MA1 (MA2) followed by a lowpass filter F1 (F2) connected to a downstream mixer MV1 (MV2).

25 The respective outputs from the downstream mixers are connected to the two inputs of a subtracter STR whose output is connected to the output terminal BS of the filter. In this circuit, likewise, the upstream mixers receive transposition signals in phase quadrature SM1 and  
30 SM2, and the two downstream mixers MV1 and MV2 likewise receive two transposition signals in phase quadrature.

Assuming, for simplification purposes, that the mixing signals delivered to the mixers MA1 and MA2 are respectively of the form  $\sin(\omega_0 t)$  and  $\cos(\omega_0 t)$ , and that  
35 the angular frequency of the input signal SE is  $\omega_i$ , then the signals present at the outputs of these mixers present a first frequency band centered around angular

00000000000000000000000000000000

frequency ( $\omega_i - \omega_0$ ), and a second frequency band centered around angular frequency ( $\omega_i + \omega_0$ ) which is eliminated by the lowpass filters F1 and F2.

5 The frequency  $\omega_0$  and the bandwidth B/2 of the lowpass filters F1 and F2 are selected in such a manner that the following relationship is satisfied:

$$B > 2|\omega_0 - \omega_i|$$

In other words,  $\omega_0$  is selected so as to achieve frequency transposition that matches the working 10 frequency band which is to be filtered, with the upper limit B depending on the desired selectivity.

On output from the lowpass filters F1 and F2, the signals in the two branches are subjected to frequency transposition by multiplication with signals oscillating 15 at angular frequency  $\omega_1$ .

The two transposition signals applied to the upstream mixers are at an angular frequency  $\omega_0$  which is different from the angular frequency  $\omega_1$  of the two transposition signals applied to the downstream mixers.

20 The signals obtained at the output from the downstream mixers in each of the two branches then present a first frequency band centered around the angular frequency ( $-\omega_i + \omega_0 + \omega_1$ ) and a second frequency band centered around the angular frequency ( $\omega_i - \omega_0 + \omega_1$ ).

25 The signals SMV1 and SMV2 injected respectively into the downstream mixers MV1 and MV2 are in phase quadrature, as are the signals SM1 and SM2 injected into the mixers MA1 and MA2, so one of the components of each signal reaching one of the two inputs of the subtracter 30 STR cancels with the corresponding component of the signal reaching the other input of the subtracter. The remaining one of the two components in each signal adds with the remaining component in the other signal. Thus, a single frequency band is obtained at the output from 35 the subtracter STR, and in this case the band centered on angular frequency ( $\omega_i - \omega_0 + \omega_1$ ).

By a suitable choice of  $\omega_1$ , it is possible to obtain a frequency band centered on a desired working frequency.

To deliver the quadrature signals SM1 and SM2 and the quadrature signals SMV1 and SMV2, the present embodiment proposes connecting the input of the upstream mixers to a unit comprising an oscillator LO at the frequency  $\omega_0$  in association with an RC-CR type phase shifter MTM as shown in Figure 4. This phase shifter MTM comprises two phase shifting cells CD1 and CD2 disposed respectively between the output of the local oscillator LO and each of the two processing paths VT1 and VT2.

The first phase shifting cell CD1 is a capacitive-resistive cell comprising a capacitor C having one terminal connected to an output of the oscillator LO and having its other terminal connected to ground via a resistor R. The other phase shifting cell CD2 is a resistive-capacitive cell comprising a same-resistance resistor R with one terminal connected to the output of the oscillator LO and its other terminal connected to ground via a capacitor C identical to that of the cell CD1.

After passing through the phase shifting cell CD1, the signal SL output by the local oscillator LO is subject to a phase shift of  $90^\circ - \theta$  (e.g.  $45^\circ$ ) as defined by the product  $R \times C$  so as to deliver the first mixing signal SM1. Since the resistance and capacitance R and C of the cell CD1 and of the cell CD2 are identical, the cell CD2 serves to deliver a second mixing signal SM2 of phase shift equal to  $-\theta$  relative to the output signal from the oscillator LO.

The downstream phase shifter MTV receives an input sinewave signal at an angular frequency  $\omega_1$ . This input signal is generated by frequency division from the signal output at  $\omega_0$  by the oscillator LO, using a conventional type of frequency divider.

In a variant, the two input signals for the phase shifters, respectively at  $\omega_0$  and at  $\omega_1$ , can both be

obtained by frequency division from a signal output by an oscillator, with the two division ratios naturally being different.

The ratio between the two frequencies  $\omega_0$  and  $\omega_1$  is  
5 known accurately, and is equal to a rational number, i.e.  
to a ratio of two integers.

The downstream phase shifter MTV is similar in structure to the upstream phase shifter MTM, as can be seen in Figure 5. For this second phase shifter, two  
10 capacitors  $C'$  are used, each having capacitance equal to  $mC$  where  $m$  is an integer. The two resistors  $R'$  of this phase shifter each has resistance equal to  $nR$  where  $n$  is an integer.

In a variant, the ratio  $m$  between  $C'$  and  $C$  can be a  
15 rational number, as can the ratio  $n$  between  $R'$  and  $R$ .

The resistances and the capacitances in the circuits MTM and MTV are selected so that the cutoff angular frequencies of each of these two circuits are equal to the angular frequencies of the signals they receive at  
20 their respective inputs, so as to obtain the same gain in both filtering paths VT1 and VT2 for both phase shifters.

The resistors and capacitors in the second phase shifter MTV thus have resistances and capacitances  $R'$  and  $C'$  selected so that the ratio  $R'C'/RC$  is equal to the  
25 ratio  $\omega_0/\omega_1$  of the angular frequencies of the signals delivered to said two phase shifters MTM and MTV. The ratio  $\omega_0/\omega_1$  is thus equal to a ratio of integers.

The phase shifters MTM and MTV are positioned in opposite manner so that each parallel branch VT1 and VT2 receives a phase-advanced signal on one of its phase  
30 shifters and a phase-delayed signal on its other phase shifter. In order to simplify, each of the phase shifters is assumed to deliver a sine signal and a cosine signal, with the disposition of the phase shifters being  
35 such that each branch VT1 or VT2 receives a sine signal on one of its phase shifters and a cosine signal on its other phase shifter. Each branch is connected to the R-C

cell of one of the phase shifters and to the C-R cell of the other phase shifter.

Because the input signals to the phase shifters are supplied from a common oscillator LO, in the event of either of the two frequencies  $\omega_0$  and  $\omega_1$  being slightly offset, then the same offset will appear on the other one of said two frequencies, so both phase shifters will present an amplitude difference between their respective outputs that is the same. Since the phase shifters are oppositely positioned, each branch receives one amplified signal and one attenuated signal, such that any amplitude differences due to possible frequency variations of the oscillators is compensated within each branch and this compensation is effective over a wide frequency band.

Advantageously,  $\omega_0$  and  $\omega_1$  are selected in such a manner that the value of  $\omega_0$  is equal to a rational number multiplied by the value of  $\omega_1$ . Thus, the desired ratio between the resistances in the two phase shifters and the desired ratio between the capacitances in the two phase shifters are rational numbers that can be obtained easily and accurately with techniques that are conventional in manufacturing integrated circuits. Furthermore, by using a ratio that is equal to a rational number for the cutoff frequencies, any difference in amplitude between sine and cosine is particularly similar in both phase shifters (ignoring errors in block matching, which will in any event be limited because of the integer ratio).

Provision is also made for these phase shifters to adopt a frequency ratio equal to an integer ratio and to adopt resistances and capacitances as a function of the integers constituting the ratio.  $C$  and  $C'$  can be selected to be equal and the resistances  $R'$  and  $R$  can be selected so that  $R'/R$ , i.e.  $\omega_0/\omega_1$  is an integer number or a rational number. Similarly,  $R$  and  $R'$  can be selected to be equal and  $C$  and  $C'$  can be selected in such a manner that  $C'/C$ , i.e.  $\omega_0/\omega_1$  is equal to an integer number or a rational number.

202500-12008650

In addition, by making both phase shifters by using a similar succession of steps on each occasion, any difference in cutoff frequency or in phase shift value due to imperfection in manufacture will arrive in similar 5 manner in both circuits, such that the difference will be compensated because these circuits are crossed-over.

The use of RC-CR type phase shifters nevertheless enables good phase accuracy to be obtained.

The invention thus makes it possible to facilitate 10 complete integration of the analog portion of a receiver within an integrated circuit.